

REMARKS

Reconsideration of this application is respectfully requested.

The objection to the drawings is overcome by that accompanying newly added Fig. 8.

The rejection of claim 1-6 under 35 USC 112, first paragraph, is respectfully traversed.

The functions of the claimed modules are described in the specification of the present invention at page 7, line 24, to page 9, line 12; page 9, line 24 to page 10, line 21; page 11, line 20 to page 14, line 18; and page 15, line 1 to page 16, line 2. Those citations refer to Figs. 4-6.

In addition, the specification at page 9, lines 13-23, discloses the following:

The logical operations of the various embodiments of the present invention are implemented (1) as a sequence of computer implemented acts or program modules running on a computing system and/or (2) as interconnected machine logic circuits or circuit modules within the computing system. The implementation is a matter of choice dependent on the performance requirements of the computing system implementing the invention. Accordingly, the logical operations making up the embodiments of the present invention described herein are referred to variously as operations, structural devices, acts or modules. It will be recognized by one skilled in the art that these operations, structural devices, acts and modules may be implemented in software, in firmware, in special purpose digital logic, and any combination thereof without deviating from the spirit and scope of the present invention as recited within the claims attached hereto.

Knowing the functions as clearly enabled in the specification as described above, the skilled artisan can either implement the method in firmware or some combination of firmware and hardware without undue experimentation.

The Office Action provides absolutely no particulars about what is not enabled of claim 1. For example, equations 6-8 are preferred equations of the present invention. Why a skilled artisan would not know how to program those equations or to implement those equations as some combination of firmware and hardware is not clear from the present Office Action. Also, the claim 1 feature of a summing module operable to sum the integer portion and the fractional portion to obtain the square root is fundamental. In view of that, this rejection is erroneous. As such, claim 1 does not lack enablement and is allowable. Claims 2-6 are also allowable for the same reasons as claim 1.

If the next Office Action continues with this rejection, it is respectfully requested to set out with specificity what features of claim 1 are not enabled, particularly what feature would require undue experimentation.

The rejection of claims 7-10 under 35 USC 101 is overcome by the amendment to claim 7.

Conclusion

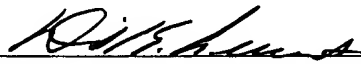
This Reply is believed to be responsive to all points raised in the Office action. Accordingly, prompt allowance and passage of the application to issue are earnestly solicited. Should the Examiner have any remaining questions or concerns, he/she is encouraged to contact the undersigned attorney by telephone to expeditiously resolve such concerns.

Respectfully submitted,

SEAGATE TECHNOLOGY LLC
(Assignee of Entire Interest)

Date

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